



PTO/SB/08a (08-03)

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)		Complete if Known			
		Application Number	10/827,178		
		Filing Date	March 19, 2004		
		First Named Inventor	Michael D. McJimsey		
		Art Unit	2838		
Sheet	1	of	2	Examiner Name	Not yet assigned Sterrett
				Attorney Docket Number	420841

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code ² (if known)			
JDA	A	US- 4,456,872	06-28-1984	Froeschle	
		US-			
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FOREIGN PATENT DOCUMENTS						
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Examiner Signature	Sterrett	Date Considered	3/16/06
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Substitute for form 1449B/PTO		Complete If Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT		Application Number	10/827,178
		Filing Date	April 19, 2004
		First Named Inventor	Michael D. McJimsey
		Art Unit	2838
		Examiner Name	Not yet assigned Sterrett
(Use as many sheets as necessary)		Attorney Docket Number	420841
Sheet	2	of	2

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
AS	B	ARBETTER, B. and MAKSIMOVIĆ, D.; "Control method for low-voltage DC power supply in battery-powered systems with power management". IEEE Power Electronics Specialists Conference, St. Louis, Missouri, June 22-27, 1997.	
	C	LEVIN, G. and O'MALLEY, K.; "Designing with hysteretic current-mode control". EDN Access, April 28, 1994, http://www.reed-electronics.com/ednmag/archives/1994/042894/09d3.htm	
	D	LEVIN, G. and O'MALLEY, K.; "Designing with hysteretic current-mode control". EDN Access, April 11, 1996 http://www.reed-electronics.com/ednmag/archives/1996/041196/08d5.htm	
		Analog Devices, Inc., Multiphase IMVP-IV Core Controller for Mobile CPUs, ADP3205, 2003, pp. 1-22	

Examiner Signature	Sterrett	Date Considered	3/16/06
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